Q.P. Code: 18EE2112

SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY::PUTTUR (AUTONOMOUS)

M.Tech I year II Semester (R18) Regular Examinations June 2019 (For Students admitted in 2018 only)

Time: 3 hours POWER ELECTRONIC CONVERTERS Max. Marks:60

(Power Electronics)

(Answer all Five Units 5×12=60 Marks)

		UNIT I	
1	a.	Explain briefly about MOSFET and Draw the switching characteristics of MOSFET	6M
	b.	Explain about output and transfer characteristics of IGBTs	6M
		OR	
2	a.	What are the turn-off and turn-on characteristics of SCR	6M
	b.	What are the purpose of shunt snubber and series snubber in transistor	6M
		UNIT II	
3	•	Explain the principle of operation of phase-controlled converter	6M
3	a. b.	How does a 12 pulse converter works? and draw the circuit	6M
	D.	OR	OIVI
4	a.	Draw the circuit arrangements of single-phase semi-converters and derive the	6M
7	а.	output voltage of semi-converter	OIVI
	b.	State and explain different methods of control of converters	6M
	υ.	UNIT III	UIVI
5	a.	Explain the principle and operation of the step-up converter with RL-load	6M
	b.	Explain the principle and operation of the cuk converter	6M
		OR	
6		Explain the principle and operation of the Buck-Boost converter	12M
		UNIT IV	
7	a.	Explain the principle and operation of the current source inverters	6M
•	b.	Compare the modulation techniques used in inverters	6M
		OR	
8	a.	Evaluate the voltage control of Three-Phase inverters	6M
	b.	Draw and explain the waveforms for three-phase inverter when each transistor	6M
	D.	conducts for 120°	OIVI
		UNIT V	
		ONT V	
9	a.	Explain the principle of the Three-Phase Inverter	6M
	b.	Explain the principle and operation of the parallel inverters	6M
		OR	
10	a.	Explain briefly about difference between voltage control and PWM technique	6M
	b.	Explain the Pulse width modulation techniques used in inverters.	6M
		END	